IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT

Inventor:

Robert D. Slater

Docket No. RAYTI.0800

Serial No.

10/772,592

Examiner: Kamini Shah

Filing Date: 2/5/2004

Art Unit:

2128

Title:

OPERATIONS AND SUPPORT DISCRETE EVENT

SIMULATION SYSTEM AND METHOD

CERTIFICATE OF CORRECTION UNDER 37 CFR 1.322 AND 1.323

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

SIR:

Please correct the above-identified patent by issuing a Certificate of Correction for a Patent Office mistake under Rule 1.322 and 1.323 as follows:

In column 28, line 13, claim 6, delete "Birth Date TTF" and replace with -Birth Date, TTF--;

In column 28, lines 47-48, claim 12, delete "corresponding different" and replace with -corresponding to different--;

In column 28, line 57, claim 14, delete "Cheek" and replace with -Check--;

In column 30, lines 7-8, claim 29, delete "a the BIT common" and replace with -the BIT common block--;

In column 32, line 13, claim 38, delete "BitDectable" and replace with -BitDetectable--;

In column 32, line 16, claim 38, delete "cateulate" and replace with -calculate--;

In column 32, line 47, claim 39, delete "function of its age" and replace with -function of its age;--;

In column 32, line 58, claim 39, delete ",said" and replace with --, said--;

In column 33, line 55, claim 41, delete "a global state: and" and replace with -a global state; and--;

In column 33, line 66, claim 42, delete "The computer-implement DES" and replace with -The computer-implemented DES--;

In column 34, line 29, claim 43, delete "time-base prediction" and replace with -time-based prediction--;

In column 34, lines 42-43, claim 46, delete "but decrease as" and replace with -but decreases as--;

In column 34, line 48, claim 47, delete " A_0 and operational" and replace with -- A_0 , and operational--;

In column 35, lines 5-6, claim 48, delete "a global state: and" and replace with -a global state; and--;

In column 35, line 14, claim 48, delete "time-base prediction" and replace with -time-based prediction--;

In column 35, line 28, claim 51, delete "but decrease as" and replace with -but decreases as--;

In column 35, line 42, claim 53, delete "A₀ and operational" and replace with -- A₀, and operational--;

In column 35, line 46, claim 54, delete "time-base prediction" and replace with --time-based prediction--;

In column 35, lines 52-53, claim 55, delete "time-base prediction" and replace with -time-based prediction--;

In column 36, lines 31-32, claim 59, delete "numbered delivered" and replace with –number delivered--.

The errors in claims 6, 12, 14, 29, 38, 39, 41, 47, 48, and 53 occurred through the fault of the Patent Office, therefore no fee is required. All other errors are the fault of the applicant, therefore the payment of the fee set forth in 1.20(a) - \$100.00 - is enclosed. The issuance of a Certificate of Correction is thus considered proper.

Date: 6-2-2008

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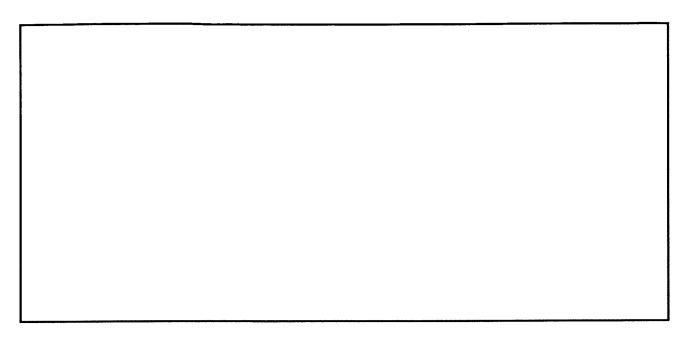
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(Also Form PTO-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE **CERTIFICATE OF CORRECTION**

·	Page1 of	1
PATENT NO. : 7,315,805 B2		
APPLICATION NO.: 10/772,592		
ISSUE DATE : January 1, 2008		
INVENTOR(S) : Slater		
It is certified that an error appears or errors appear in the above-identified patent and that sa	aid Letters Patent	
s hereby corrected as shown below:		
n column 28, line 13, claim 6, delete "Birth Date TTF" and replace with -Birth Date, TTF;		
In column 28, lines 47-48, claim 12, delete "corresponding different" and replace withcorrespond	ing to different-;	
n column 28, line 57, claim 14, delete "Cheek" and replace with –Check;		
n column 30, lines 7-8, claim 29, delete "a the BIT common" and replace with –the BIT common b	lock;	
n column 32, line 13, claim 38, delete "BitDectable" and replace with -BitDetectable;		
n column 32, line 16, claim 38, delete "cateulate" and replace with –calculate;		
n column 32, line 47, claim 39, delete "function of its age" and replace with -function of its age;;		
n column 32, line 58, claim 39, delete *,said" and replace with, said;		
n column 33, line 55, claim 41, delete "a global state: and" and replace with –a global state; and;	ı	
in column 33, line 66, claim 42, delete "The computer-implement DES" and replace with -The com	puter-implemented DES	S;
in column 34, line 29, claim 43, delete "time-base prediction" and replace with –time-based predict	ion;	
n column 34, lines 42-43, claim 46, delete "but decrease as" and replace with –but decreases as	4	
n column 34, line 48, claim 47, delete "A₀ and operational" and replace withA₀, and Operational-	- ;	
in column 35, lines 5-6, claim 48, delete "a global state: and" and replace with –a global state; and	;	
in column 35, line 14, claim 48, delete "time-base prediction" and replace with -time-based predict	ion;	
n column 35, line 28, claim 51, delete "but decrease as" and replace with -but decreases as;		
n column 35, line 42, claim 53, delete "A ₀ and operational" and replace withA ₀ , and Operational-	- ;	
n column 35, line 46, claim 54, delete "time-base prediction" and replace with -time-based predict	ion;	
n column 35, lines 52-53, claim 55, delete "time-base prediction" and replace with –time-based pre	ediction;	
n column 36, lines 31-32, claim 59, delete "numbered delivered" and replace with –number deliver	red	



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This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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